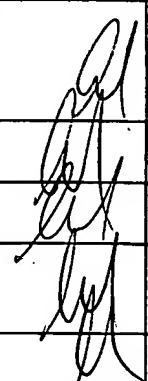


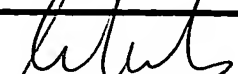
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<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>		
		<b>Application Number</b>	Not assigned	
		<b>Filing Date</b>	Herewith	
		<b>First Named Inventor</b>	Boccuzzi, Vito	
		<b>Art Unit</b>	Not assigned	
<b>Examiner Name</b>	Not assigned			
<b>Sheet</b>		<b>of</b>	<b>Attorney Docket Number</b>	BOCCUZZI 2-27-3

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the Item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T
	A	FONG, N. et al., "A 1-V 3.8-5.7-GHz Wide-Band VCO with Differentially Tuned Accumulation MOS Varactors for Common-Mode Noise Rejection in CMOS SOI Technology", IEEE Transactions on Microwave Theory and Techniques, Vol. 51, No. 8, August 2003, pp 1952-1059.	
	B	KAPUR and LONG, "IES: Efficient Electrostatic and Electromagnetic Simulation", IEEE Computational Science & Engineering, Oct-Dec 1998, pp 60-67	
	C	BEHBAHANI AND ABIDI, "RF-CMOS Oscillators with Switched Tuning", IEEE 1998 Custom Integrated Circuits Conference, pp 555 -558.	
	D	LEVANTINO et al., "Frequency Dependence on Bias Current in 5-GHz CMOS VCOs: Impact on Tuning Range and Flicker Noise Upconversion", IEEE Journal of Solid-State Circuits, Vol. 37, No. 8, August 2002, pp 1003-1011.	
	E	MAGIEROWSKI et al., "Differentially Tunable Varactor with Build-In Common-Mode Rejection", 2002 IEEE, pp 1-559-1-562.	
	F	TIEBOUT, M., "Low-Power Low-Phase-Noise Differentially Tuned Quadrature VCO Design in Standard CMOS", IEEE Journal of Solid-State Circuits, Vol. 36, No. 7, July 2001, pp 1018-1024.	

<b>Examiner Signature</b>		<b>Date Considered</b>	7/24/05
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.  
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